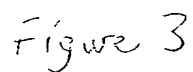
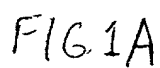


**REPORT**



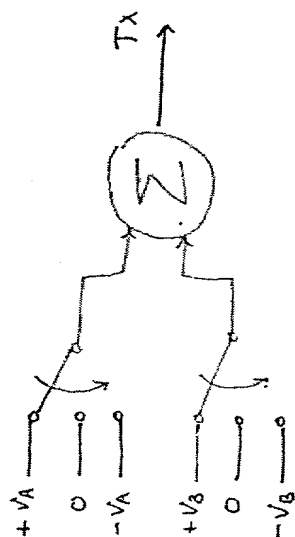
$$\begin{array}{r} V_B + V_A \\ V_B \\ V_B - V_A \\ V_A \\ 0 \\ -V_A \\ -(V_B - V_A) \\ -V_B \\ -(V_B + V_A) \end{array}$$


Fig 1B

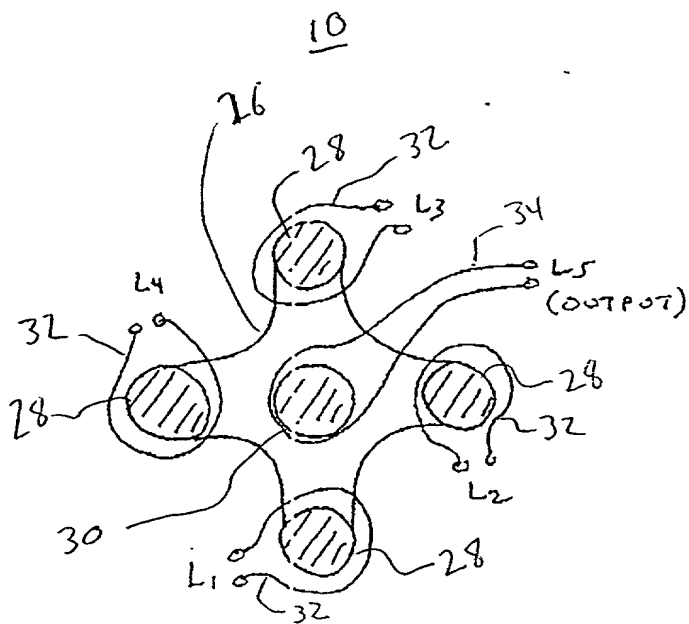


FIG. 2

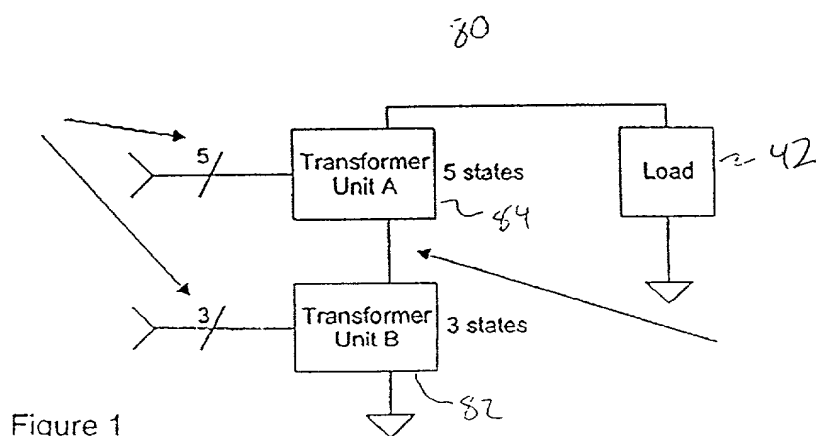


Figure 1

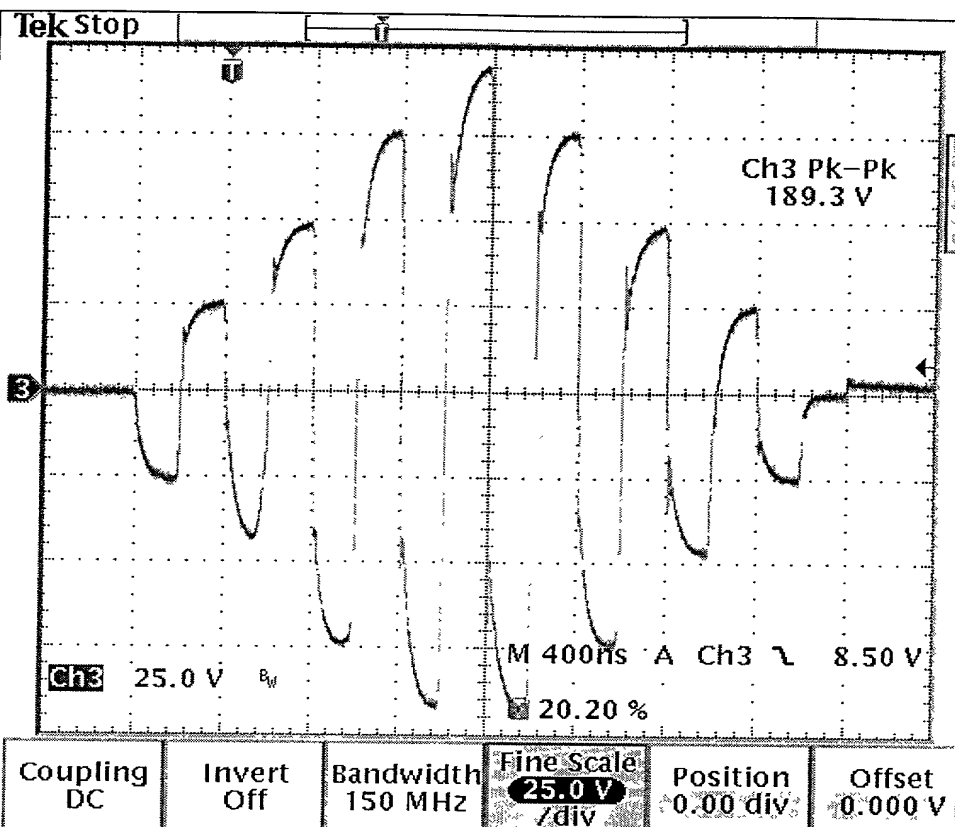
FIG 6

9-Level TX  
(50  $\Omega$  Load)

F=2.5 MHz

Vb=15V  
Va=5V

Figure 5



H2

L2

Z2

H1

L1

Z1

Control Logic

Figure 4

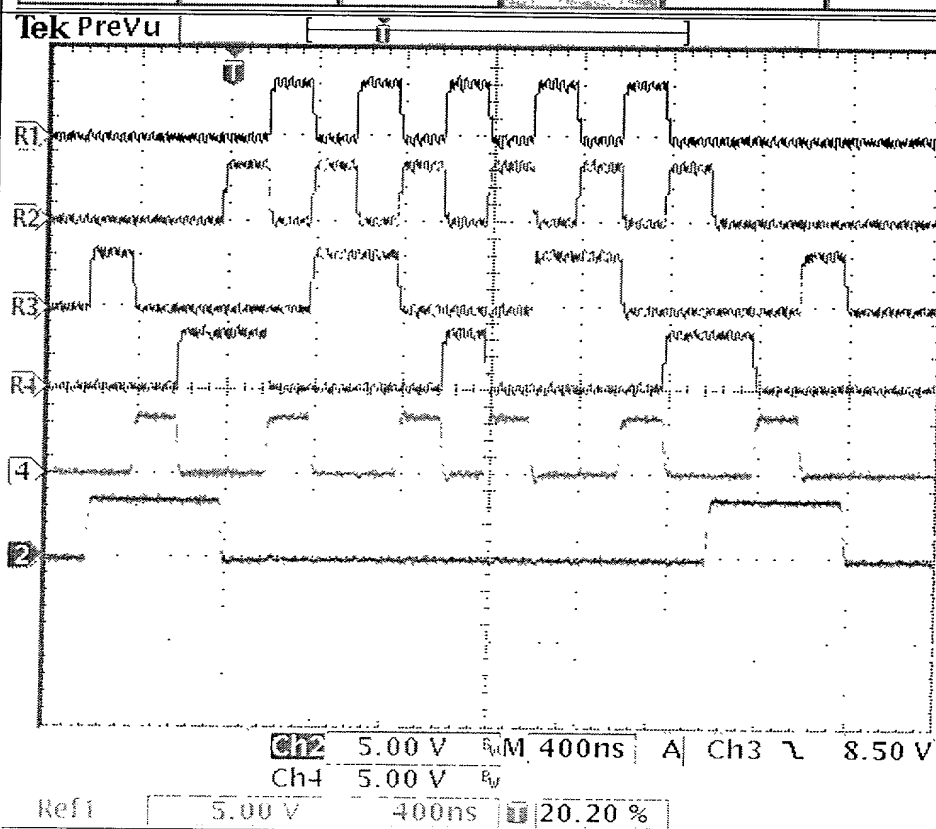


FIG. 7